

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#6 Election  
11/7/02  
VB

In re Patent Application of:

<b>Name:</b>	Zhizhang Chen, et al	<b>Date:</b>	October 29, 2002
<b>Serial No.</b>	09/817,703	<b>Examiner:</b>	Foong, Suk San
<b>Filed:</b>	March 26 2001	<b>Group Art Unit:</b>	2833
<b>PD No.</b>	10002861-1	<b>Conf. No.:</b>	9286
<b>For:</b>	LDMOS And CMOS Integrated Circuit And Method of Making		

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Assistant Commissioner for Patents  
Washington, DC 20231

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to the Election Requirement mailed October 01, 2002, Applicant elects Group I, Claims 10, 12, 14, and 16, without traverse.

Respectfully submitted,

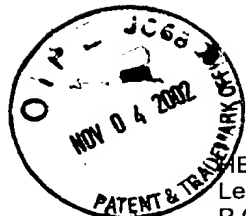
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Zhizhang Chen, et al.

By: Timothy F. Myers  
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I hereby certify that this correspondence is being deposited on **October 29, 2002**, with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington D.C. 20231.

Timothy F. Myers  
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2833

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Attorney Docket No: 10002861-1

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ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON DC 20231

TRANSMITTAL LETTER

Sir:

Transmitted herewith is/are the following in the above-identified application:

- ( ) Response After Final Under 37 CFR §1.116
- ( ) Supplemental Declaration
- (X) Other: Response to Restriction Requirement

There is no charge for this correspondence; no new or additional matters are involved.

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